

Inventors: Jayesh R. Bhakta et al.
Filed.: January 27, 2004 Atty Docket: NETL.001DV3
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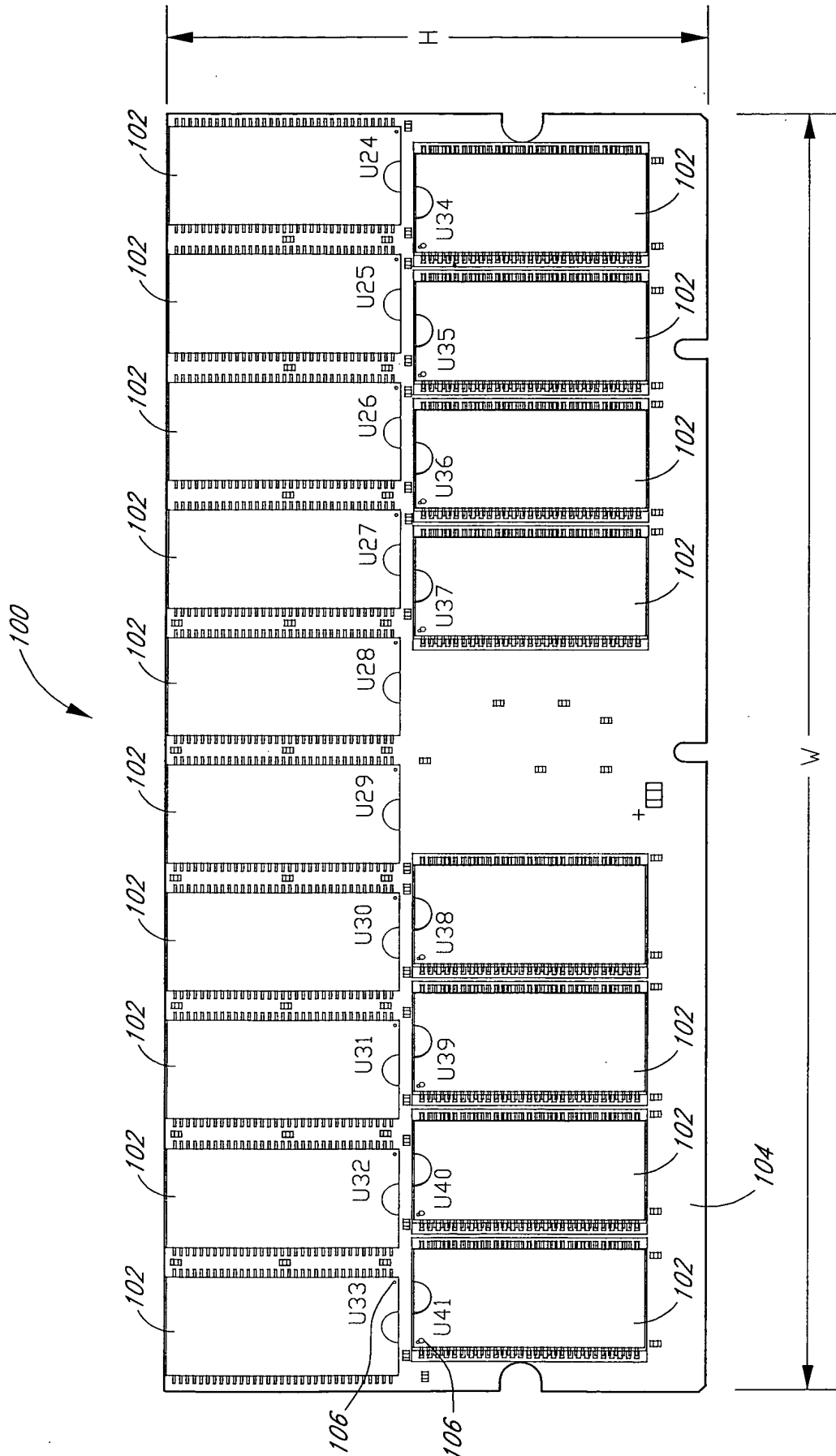


FIG. 1B

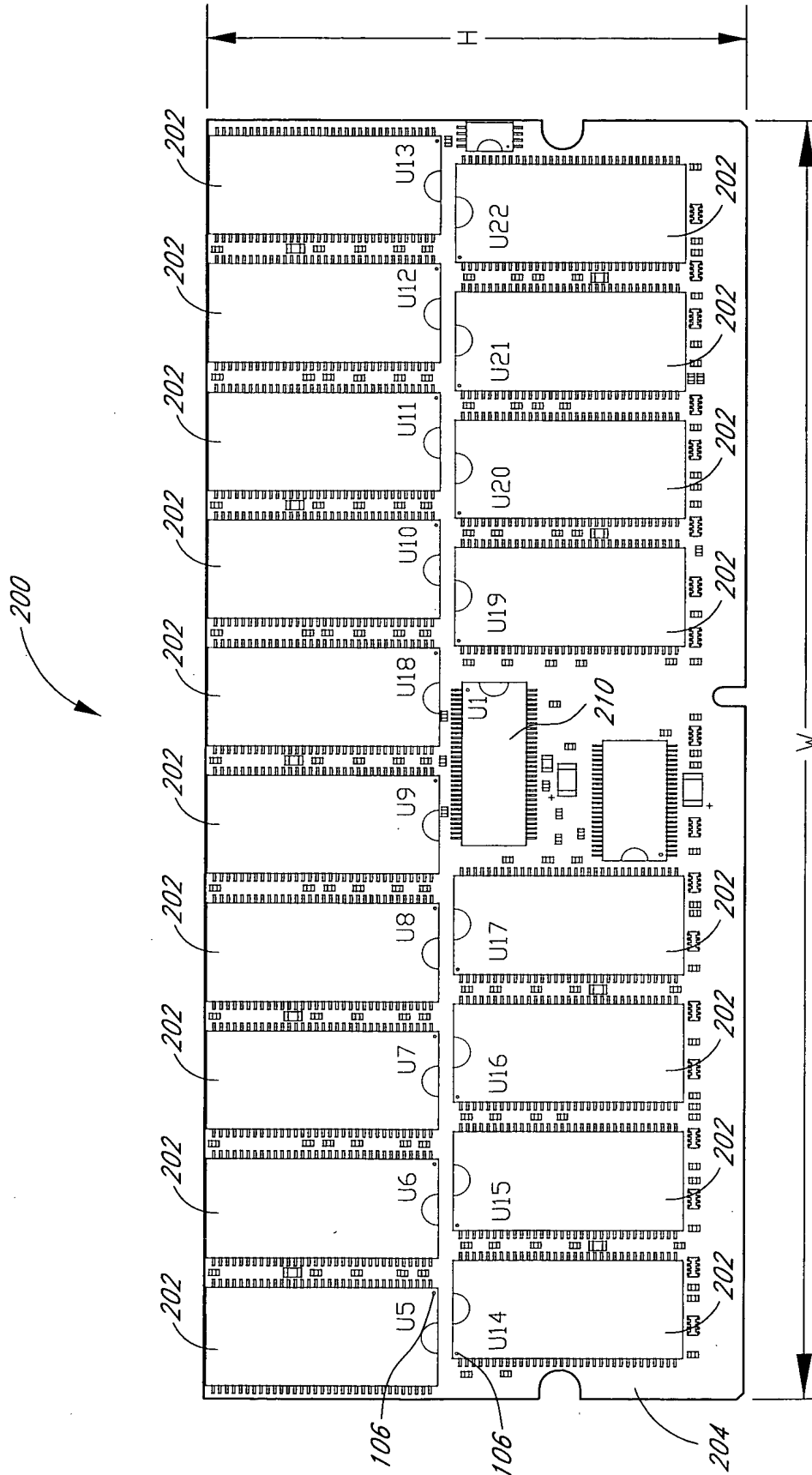


FIG. 2A

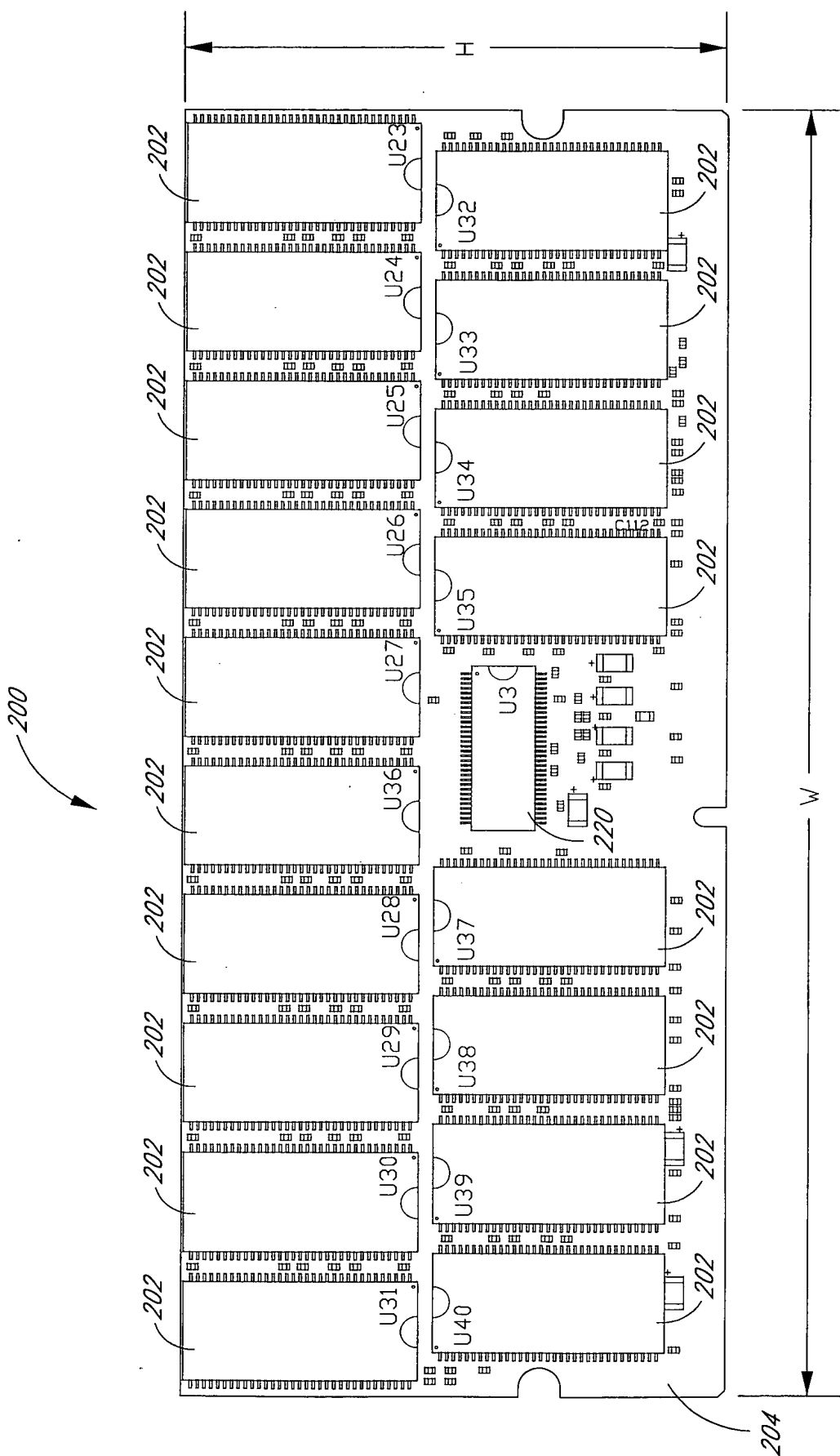


FIG. 2B

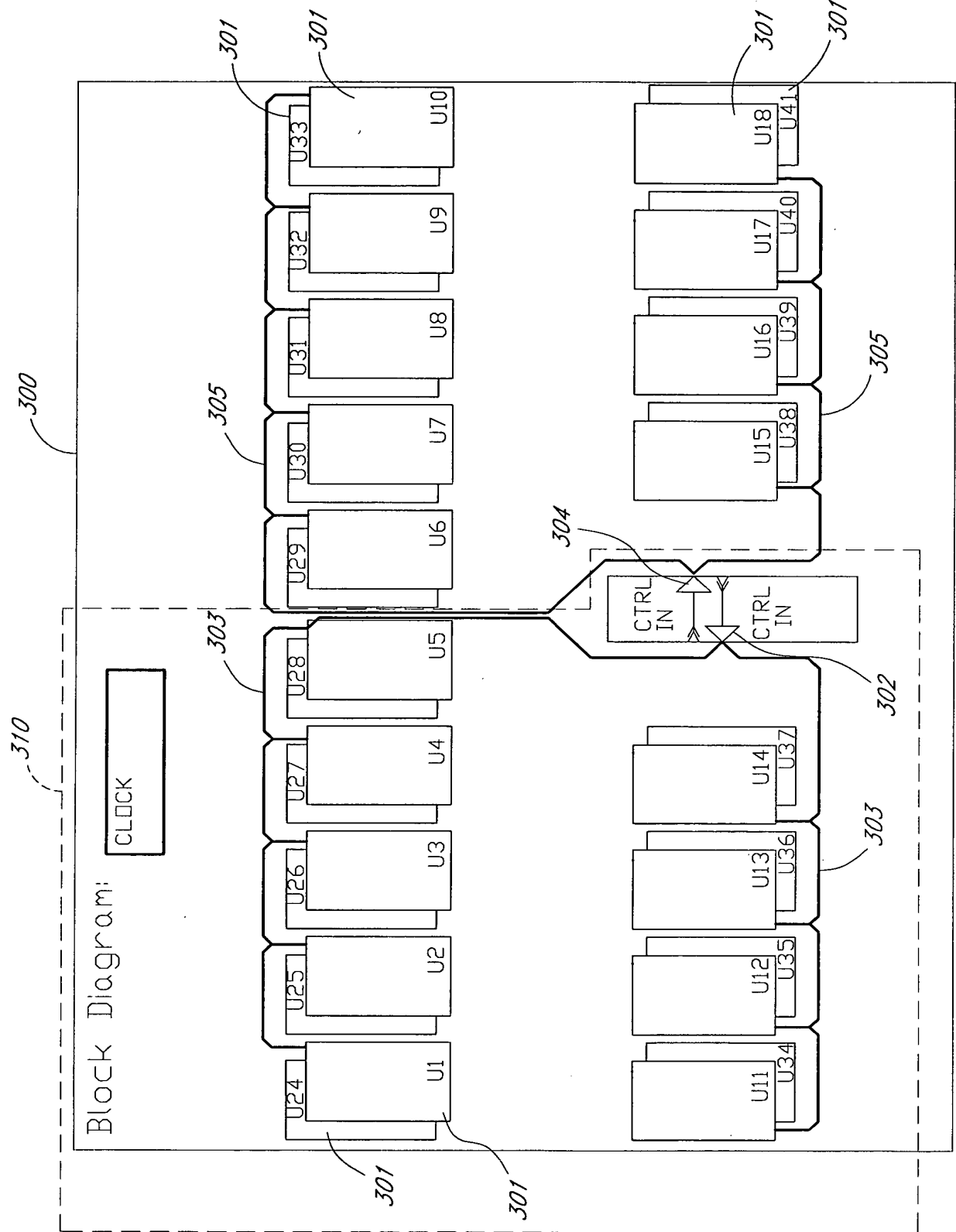


FIG. 3A

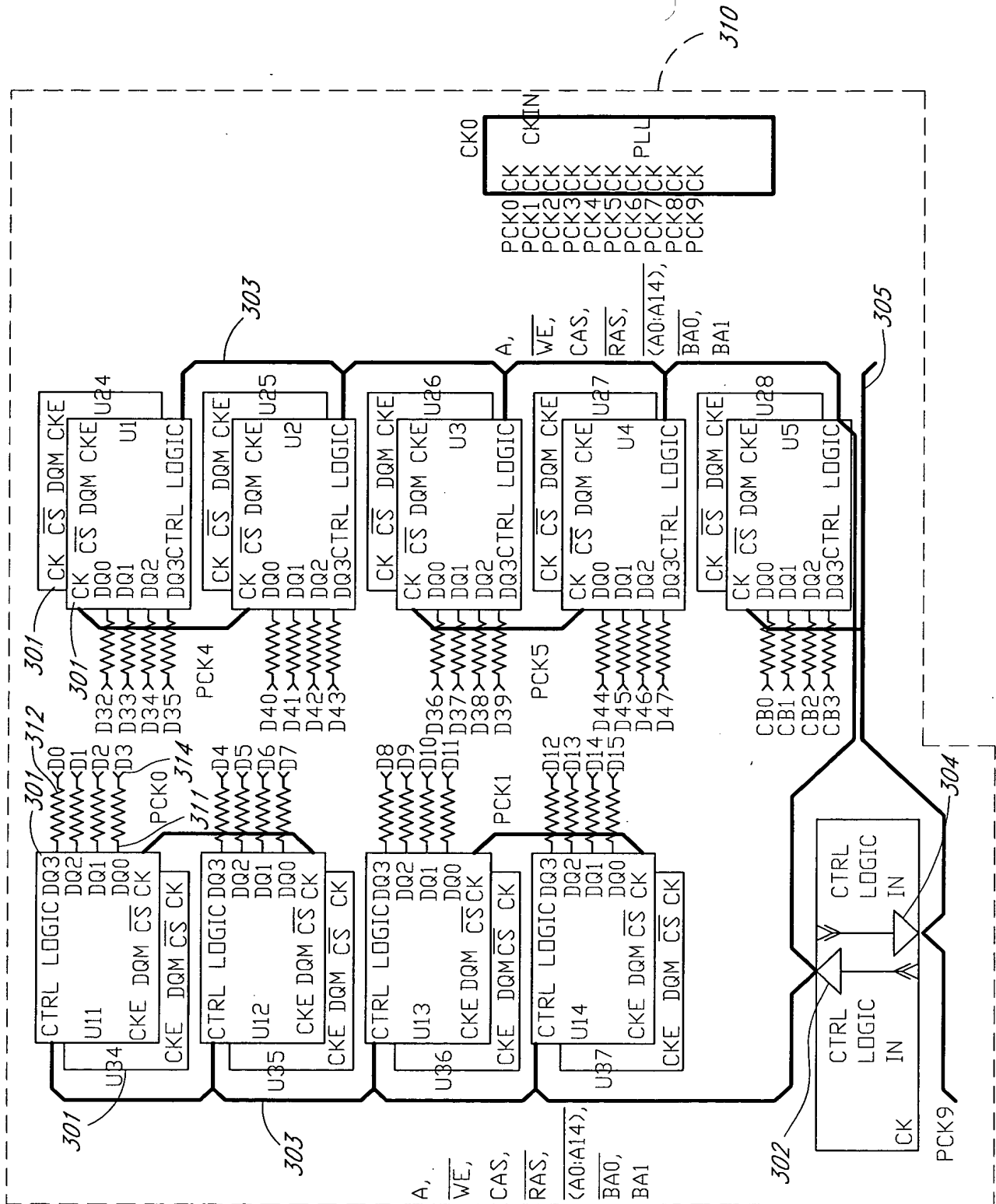


FIG. 3B

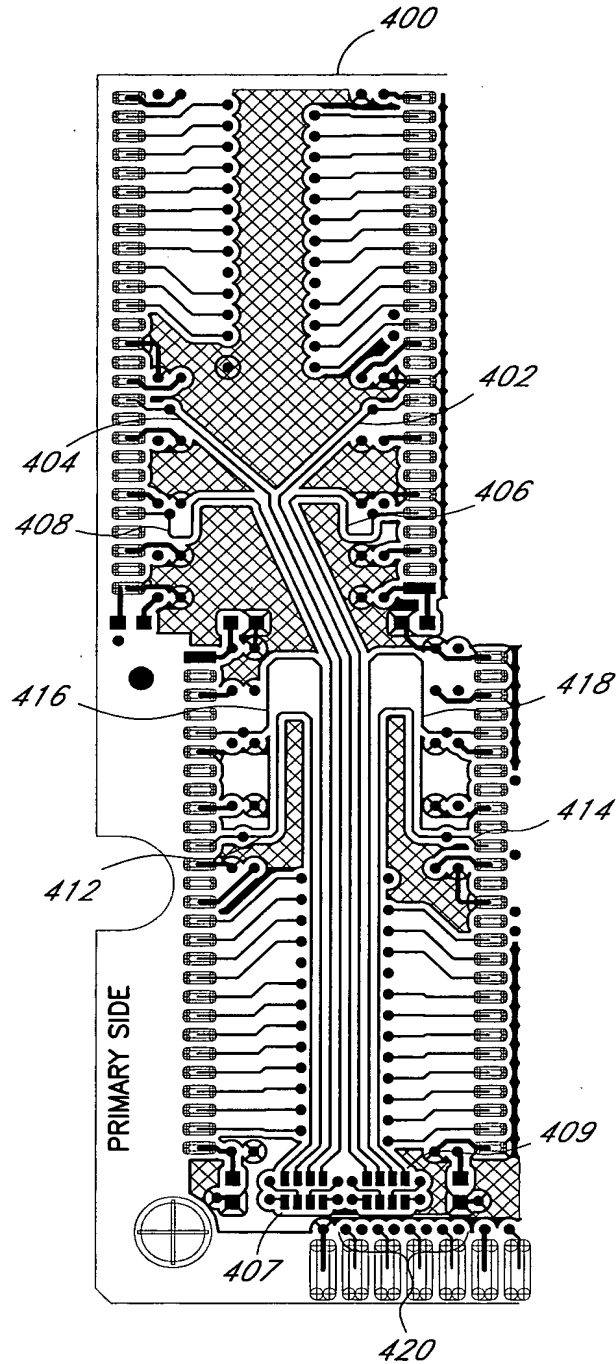


FIG. 4A

ARRANGEMENT OF INTEGRATED CIRCUITS
IN A MEMORY MODULE

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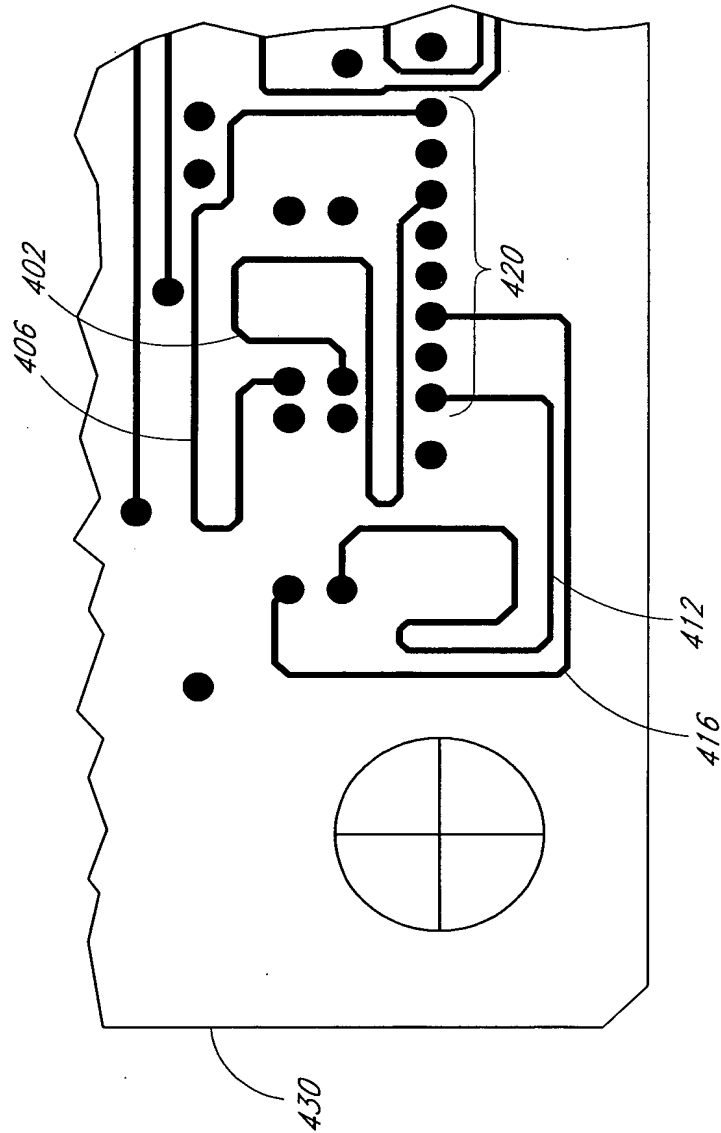


FIG. 4B

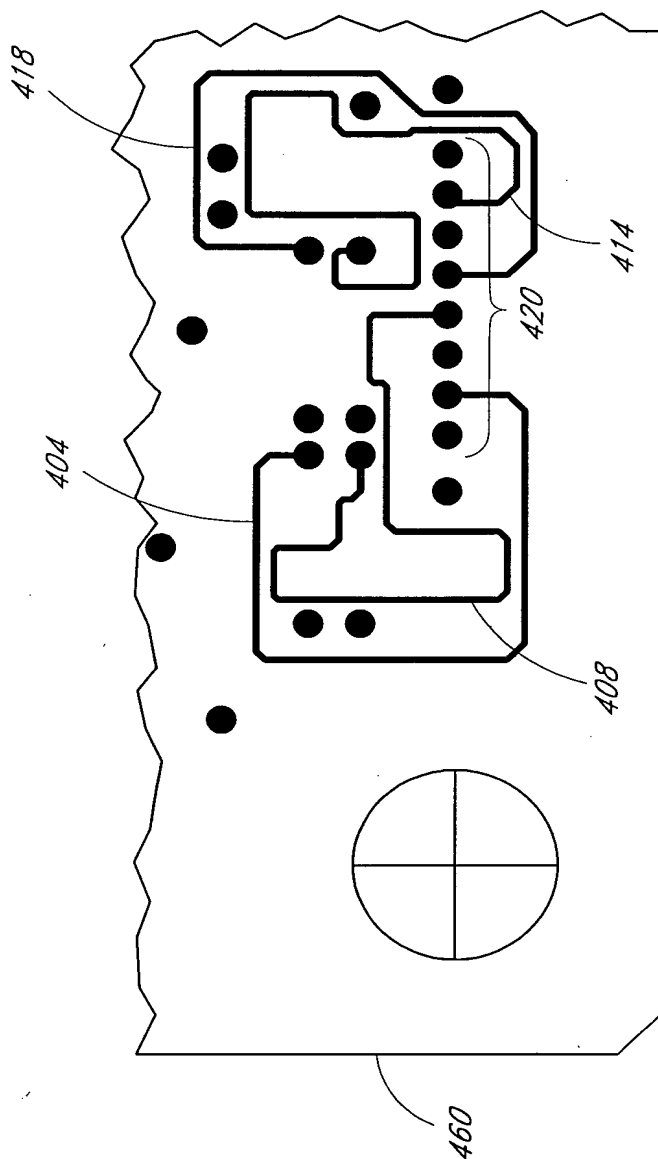


FIG. 4C